

FIG2

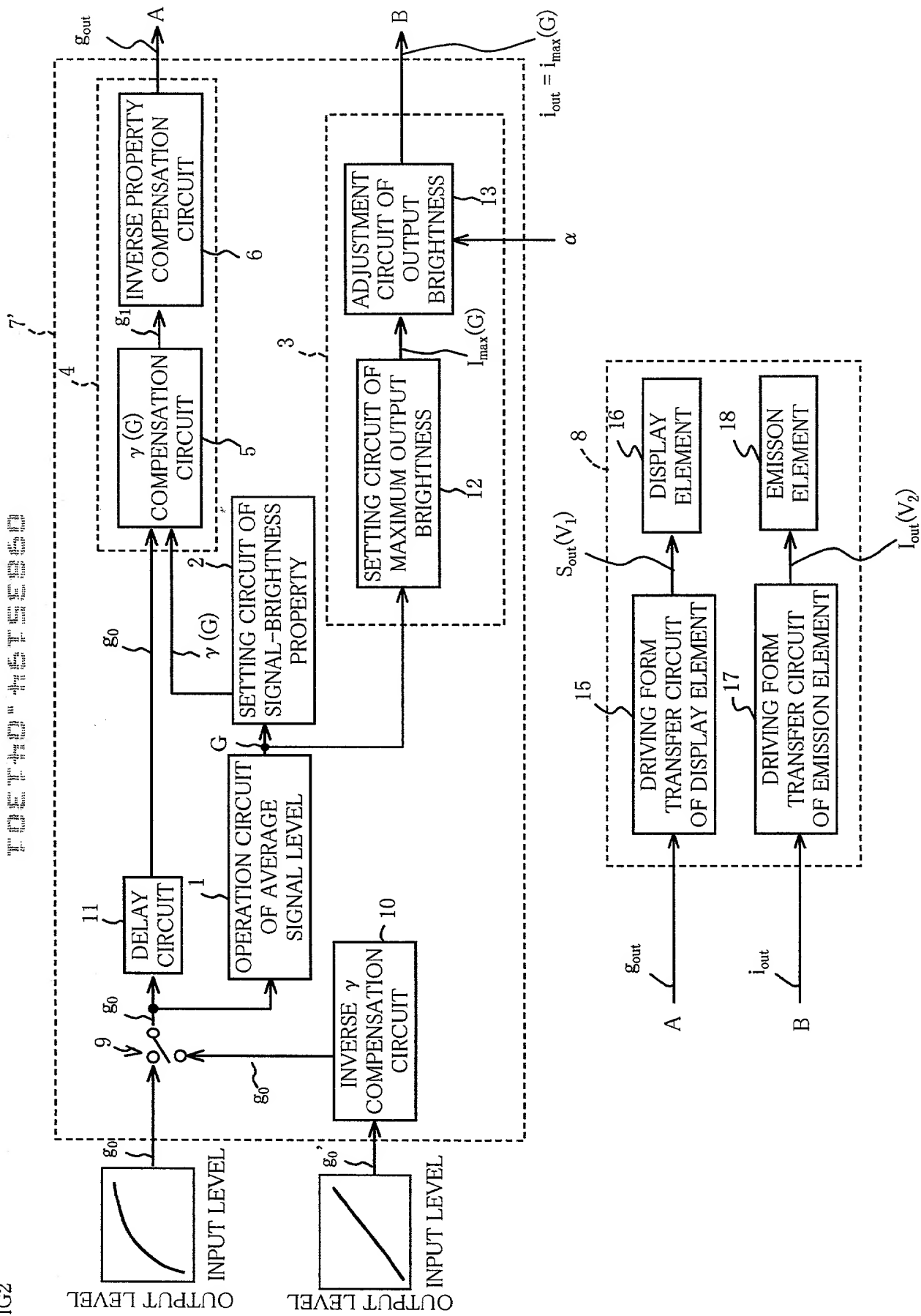


FIG 3

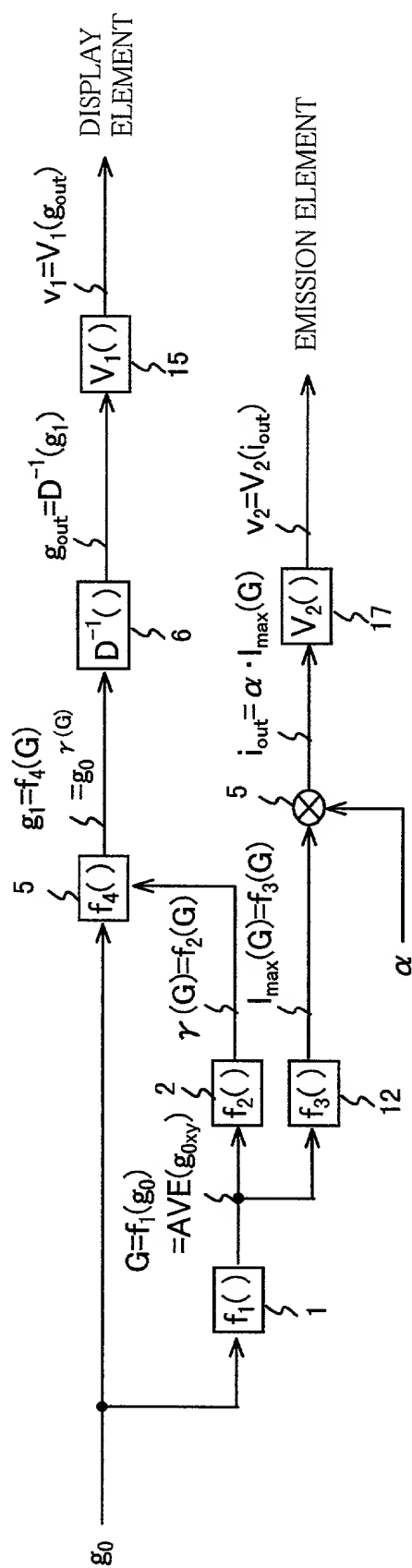
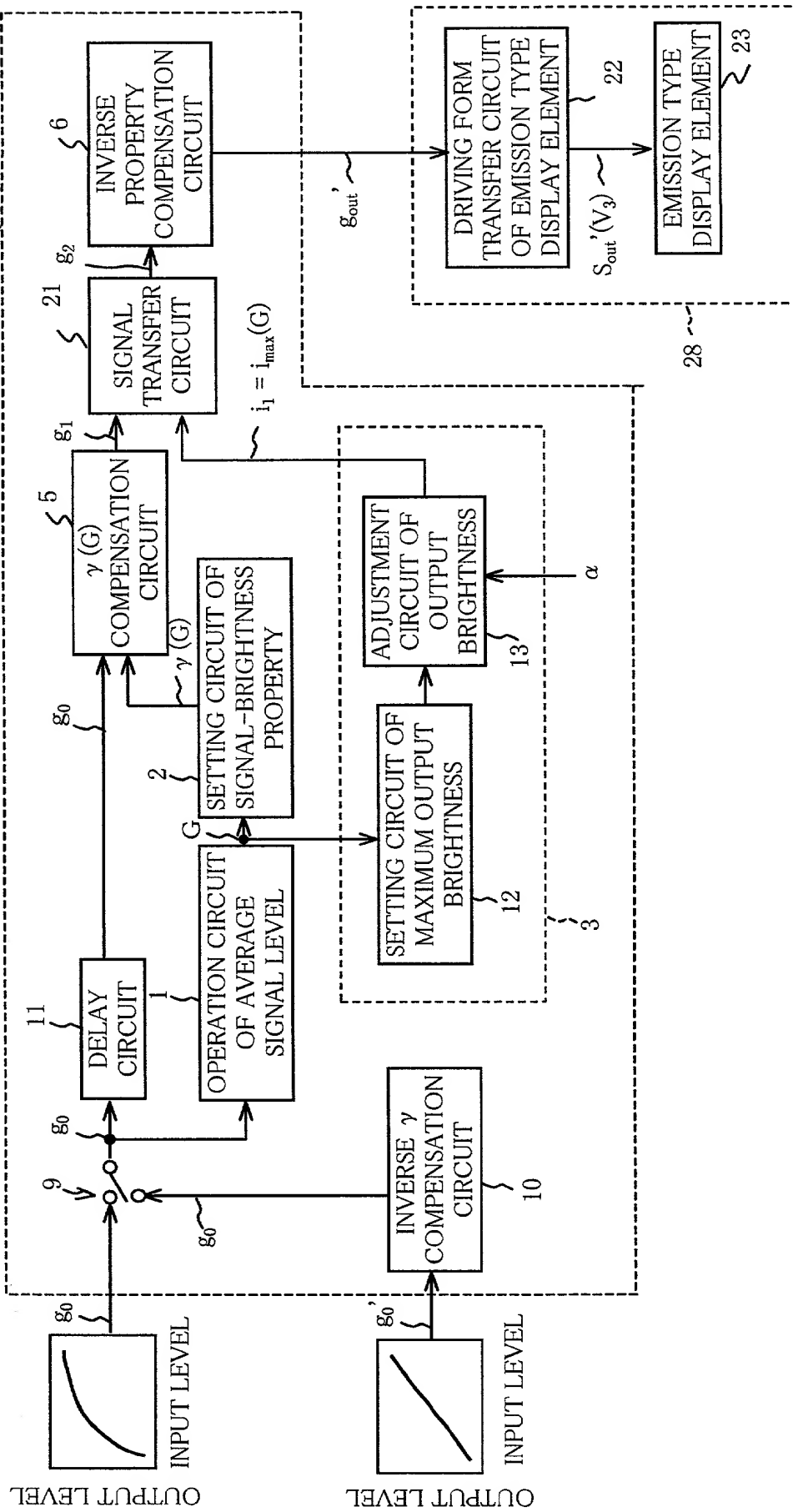


FIG 4



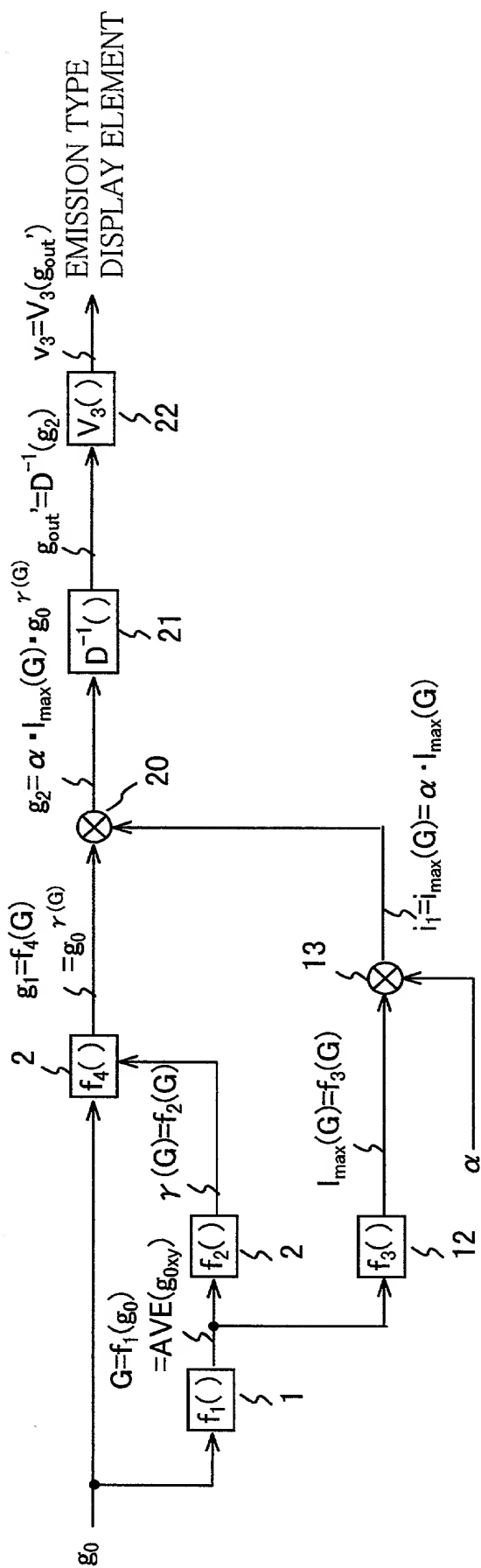
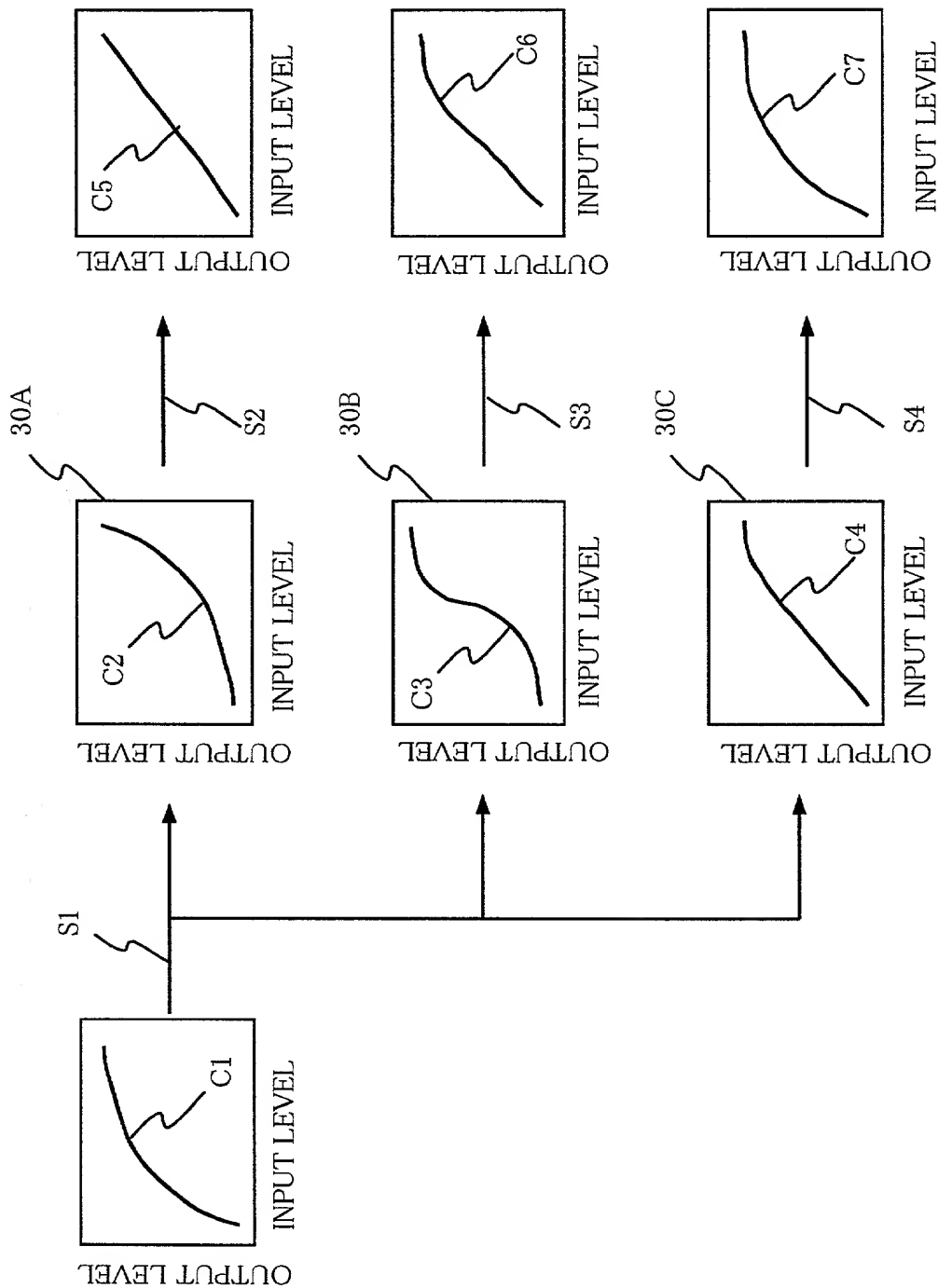


FIG 6

FIG 6 shows a schematic diagram of a system 300. The system 300 includes a first input 301, a first output 302, a second input 303, a second output 304, a third input 305, and a third output 306. The system 300 is configured to process the first input 301 and the second input 303 to produce the first output 302 and the second output 304. The system 300 is also configured to process the third input 305 to produce the third output 306.



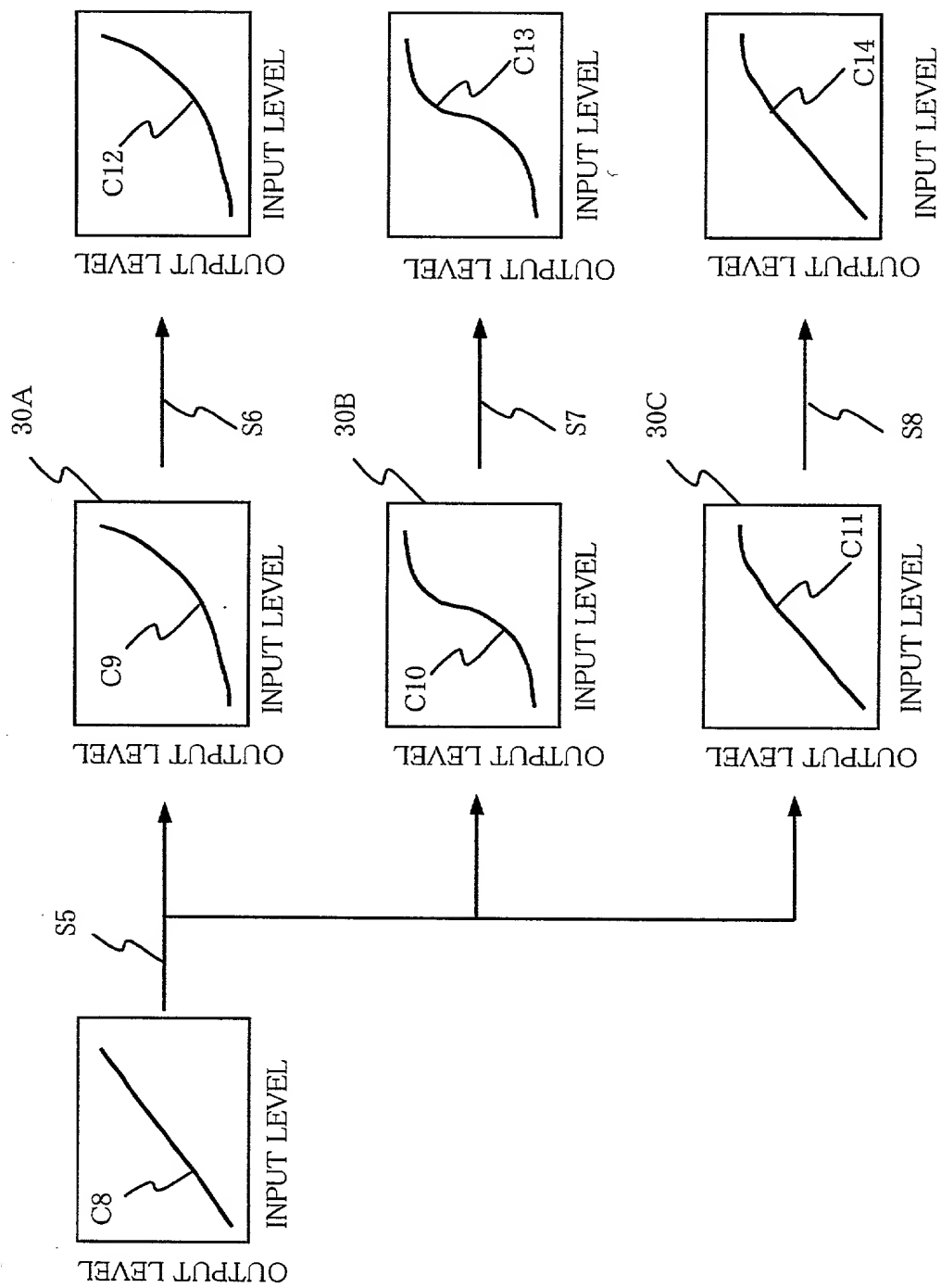
[illegible]

FIG 8

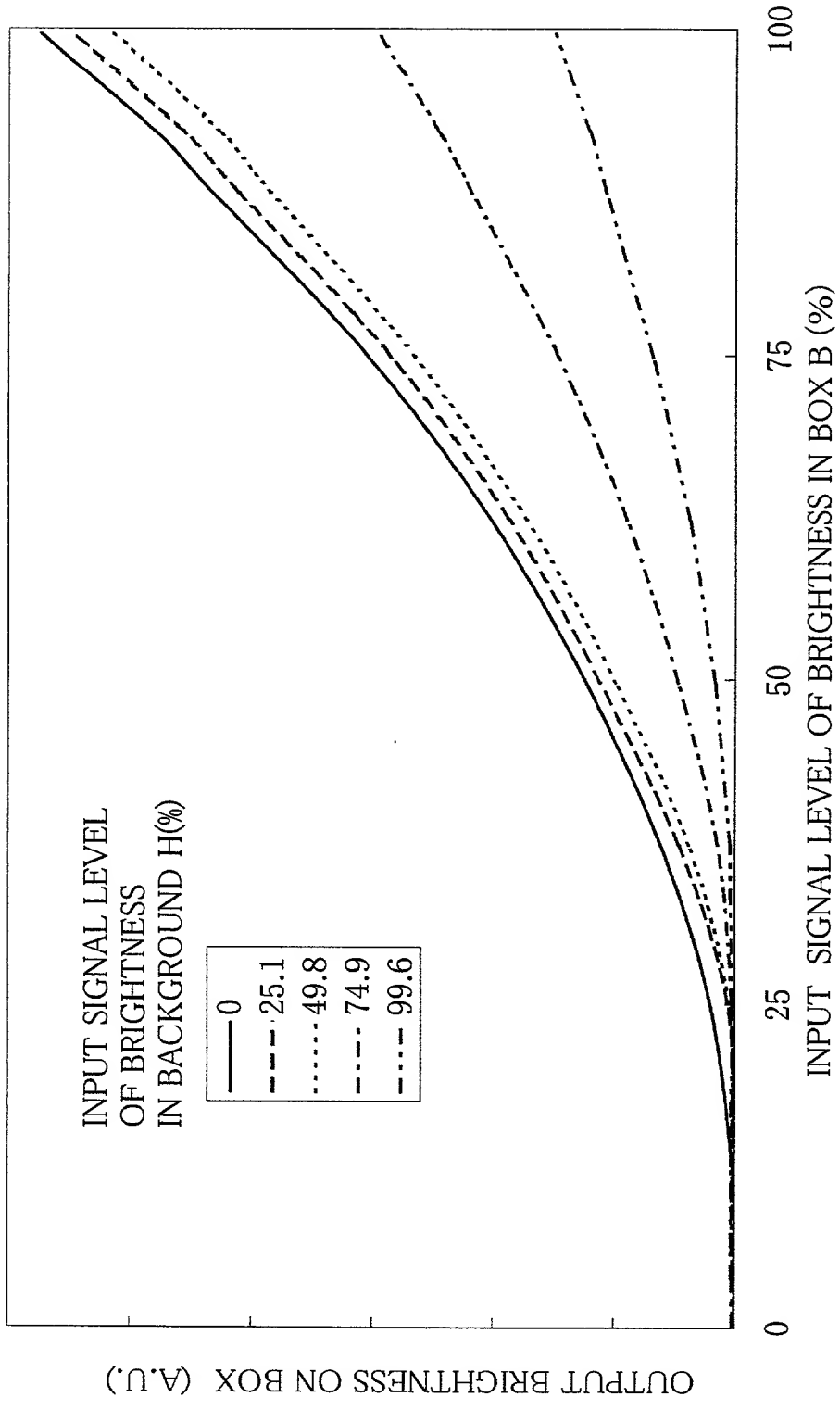




FIG 9

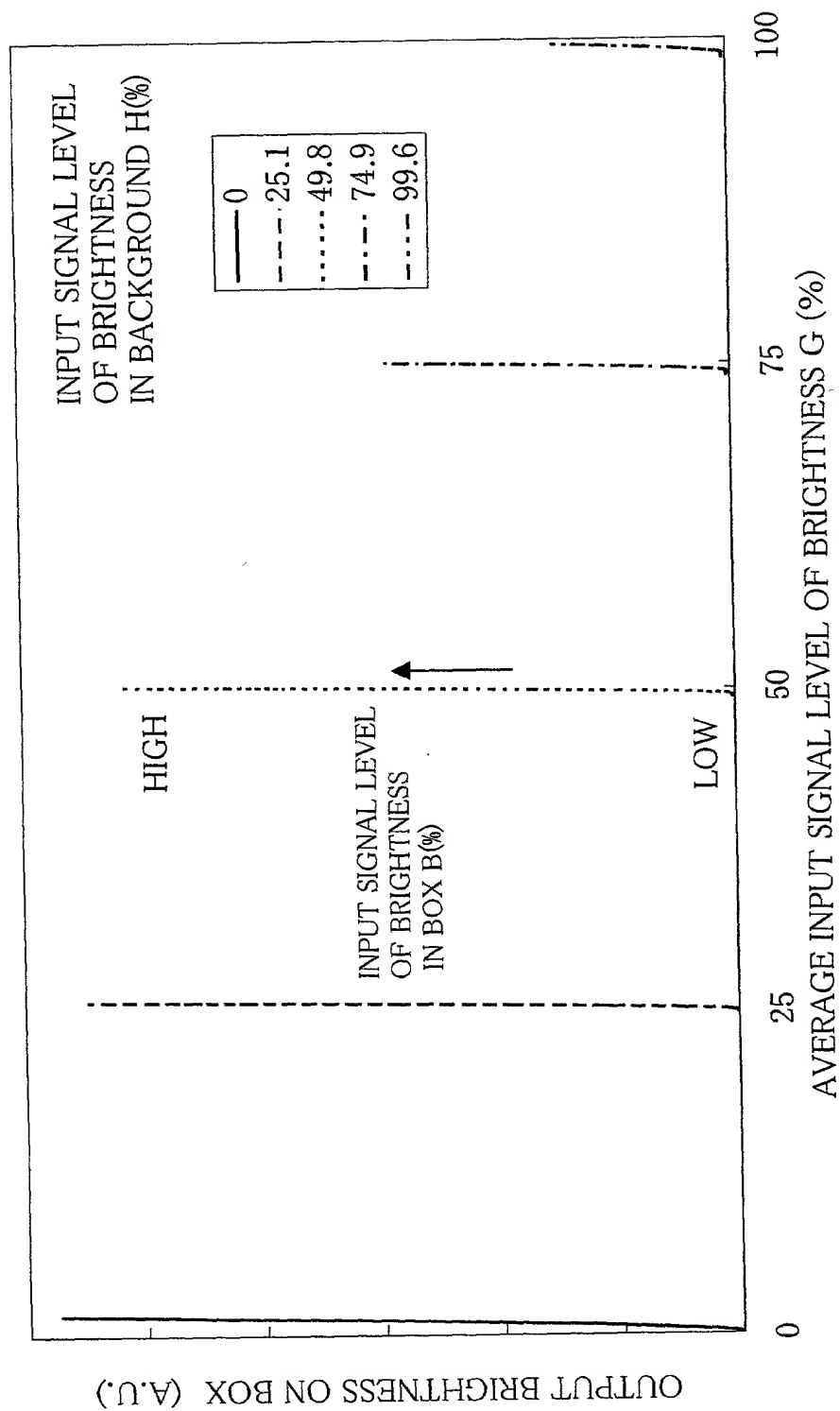


FIG 10

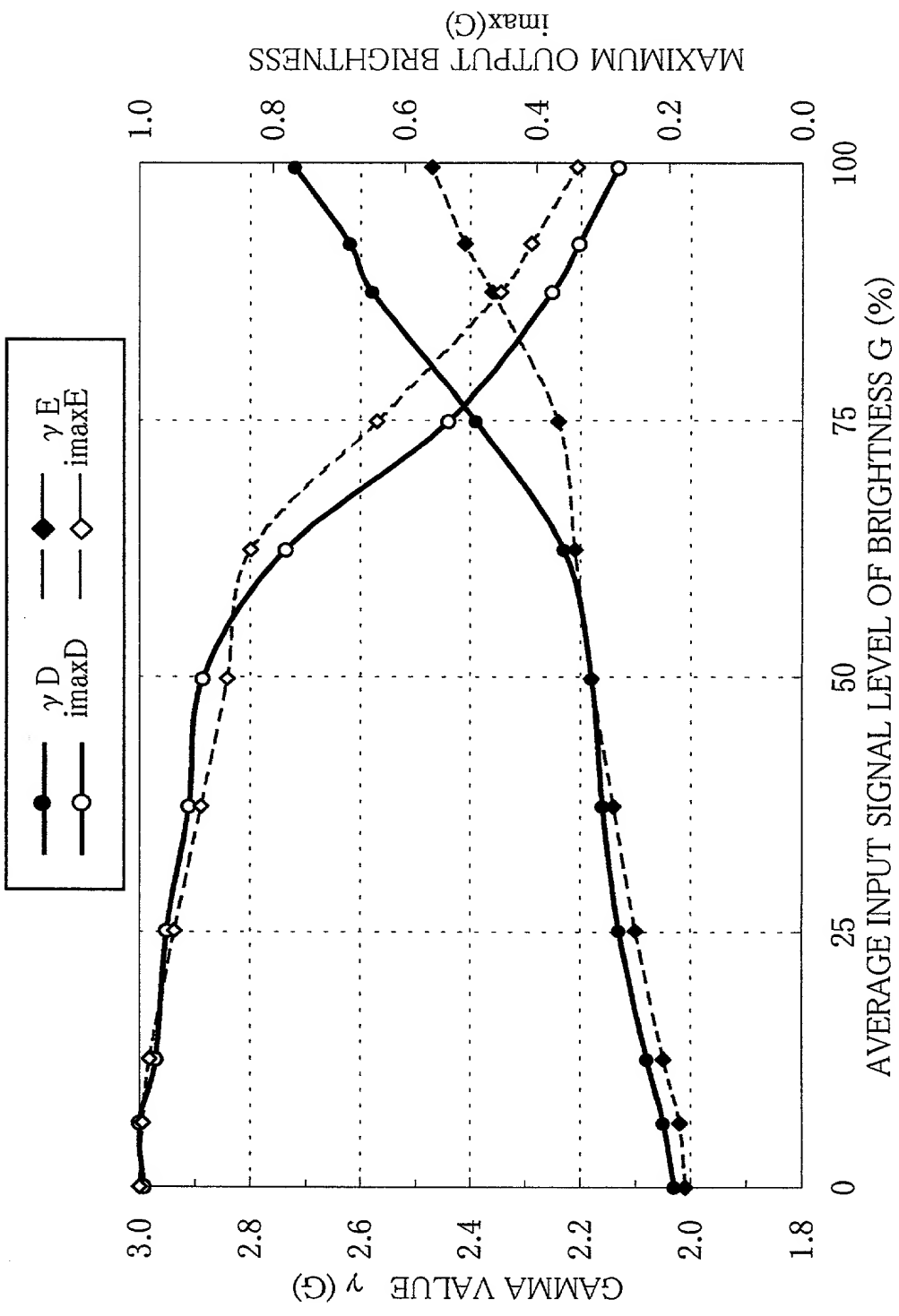


FIG 11

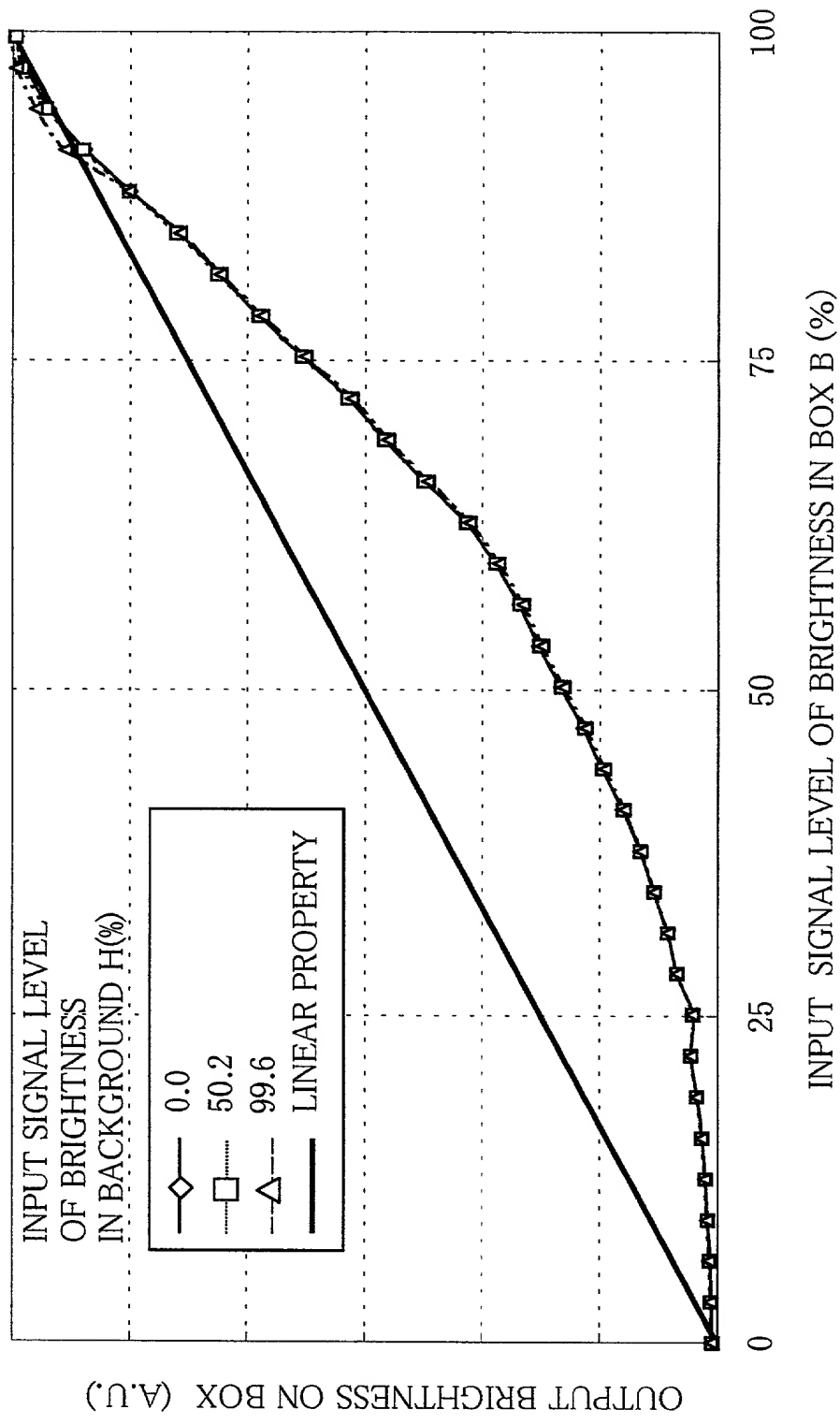


FIG 12

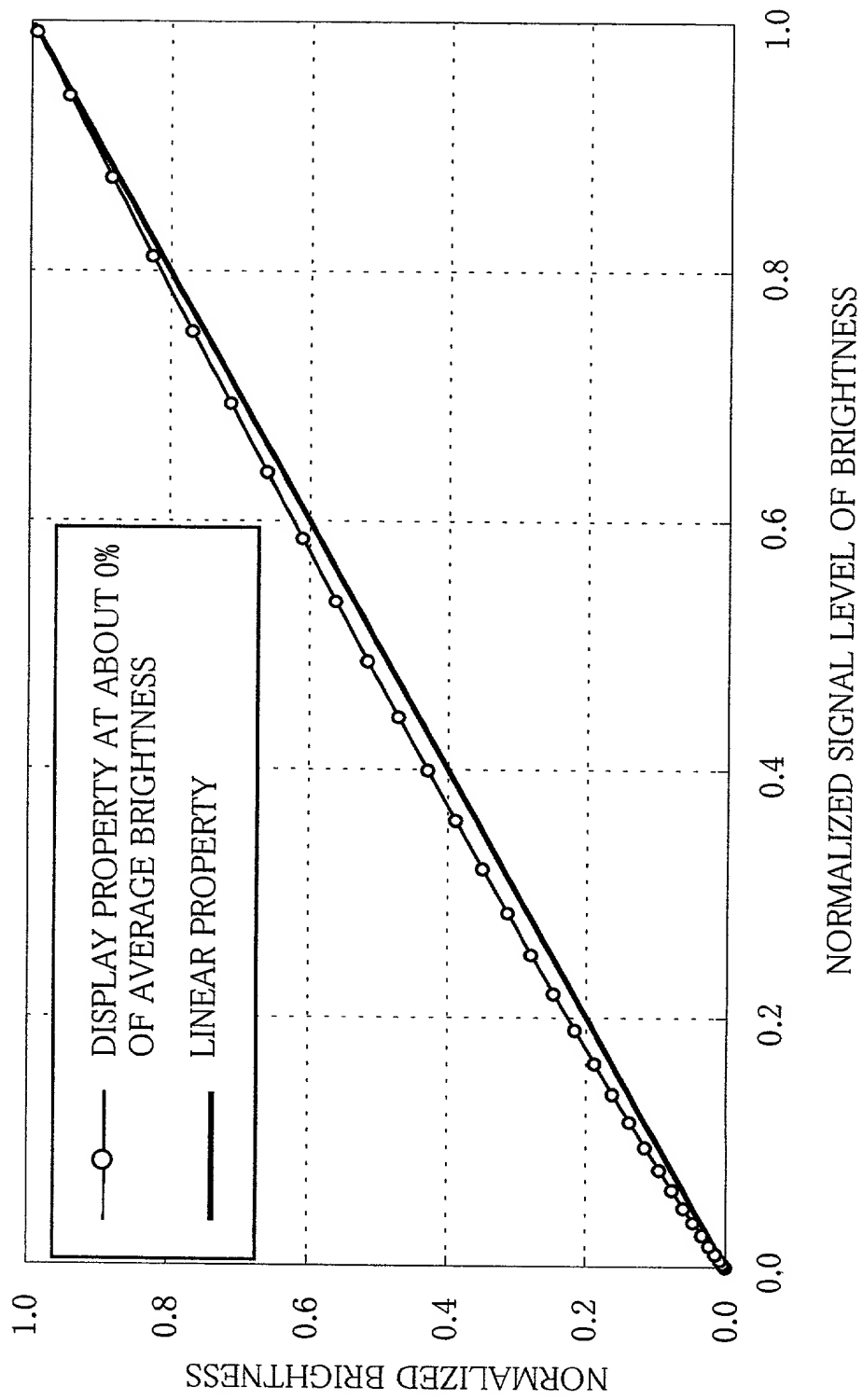


FIG 13

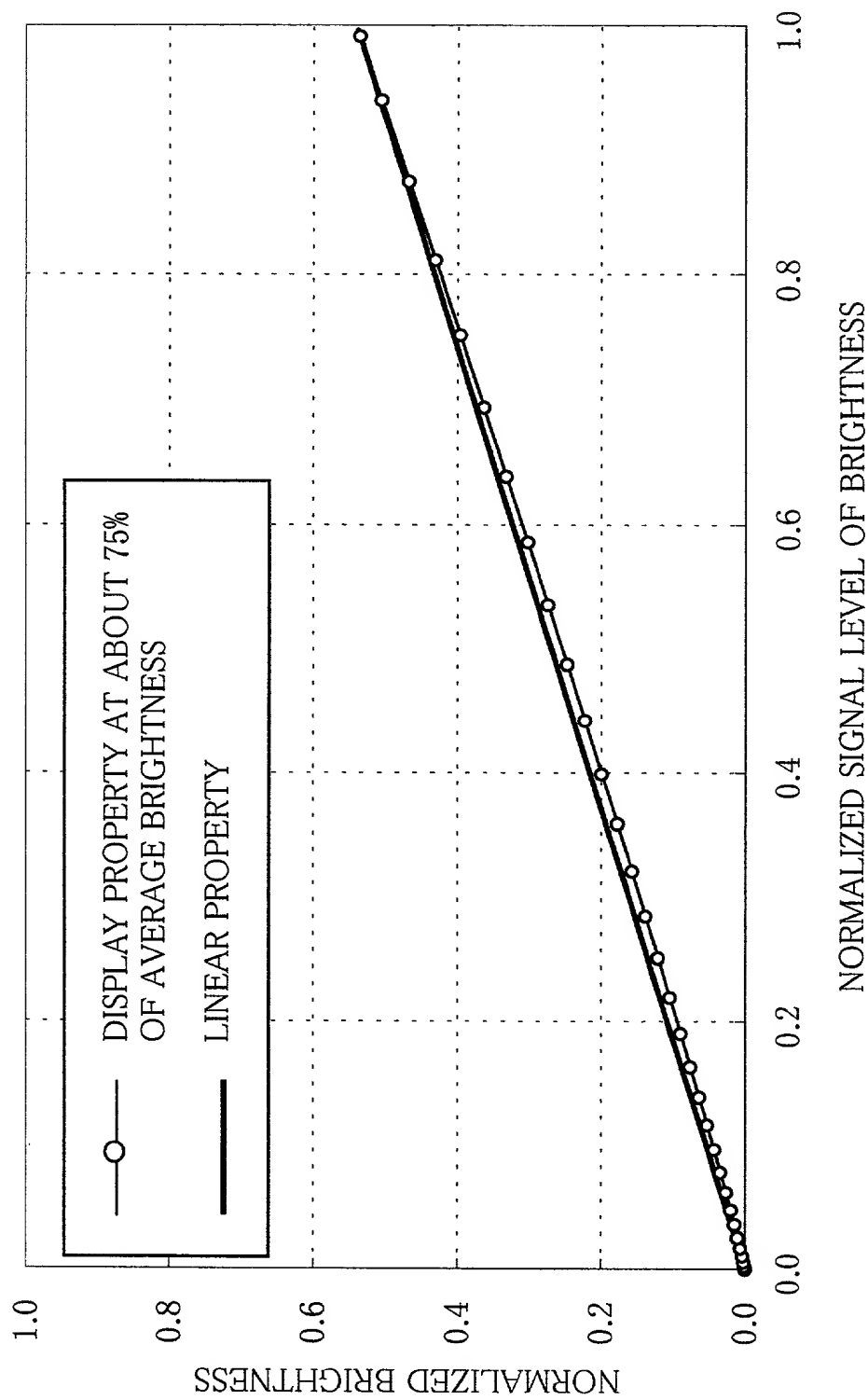


FIG 14

